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By: M. P. [Signature]

Date: 1/15/01

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a-1-02
3-15-02

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Rainald Sander
Applic. No. : 09/943,589
Filed : August 30, 2001
Title : Circuit Arrangement to Determine the Current
in a Load Transistor

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P R E L I M I N A R Y A M E N D M E N T

Hon. Commissioner of Patents and Trademarks,
Washington, D. C. 20231

S i r :

Preliminary to Examination, kindly amend the above-identified
application as follows:

In the Translated Specification:

Page 1, line 3, delete the paragraph reading, "Description".

Page 1, lines 5-6, replace the paragraph reading, "Circuit
arrangement for detecting the current in a load transistor"
with:

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